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	Application No.	Applicant(s)	,
Notice of Allowability	10/748,205	EGUCHI, FUMIO	
Notice of Allowability	Examiner	Art Unit	
	ANH PHUNG	2824	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS I herewith (or previously mailed), a Notice of Allowance (PTOL85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT I of the Office or upon petition by the applicant. See 37 CFR 1.3	S (OR REMAINS) CLOSED in this b) or other appropriate communica RIGHTS. This application is subje	s application. If not included ation will be mailed in due co	d ourse. THIS
1. \boxtimes This communication is responsive to <u>the Application filed</u>	on Dec. 31, 2003		
2. ☑ The allowed claim(s) is/are <u>3 and 4</u> .			
3. ☑ The drawings filed on <u>31 December 2003</u> are accepted b	y the Examiner.		
 4. Acknowledgment is made of a claim for foreign priority a) All b) Some* c) None of the: 1. Certified copies of the priority documents hat 2. Certified copies of the priority documents hat 3. Copies of the certified copies of the priority documents hat linternational Bureau (PCT Rule 17.2(a)). * Certified copies not received:	ve been received. ve been received in Application Not locuments have been received in the locuments have been received.	o this national stage application	
5. A SUBSTITUTE OATH OR DECLARATION must be subr INFORMAL PATENT APPLICATION (PTO-152) which give			TICE OF
 CORRECTED DRAWINGS (as "replacement sheets") m (a) including changes required by the Notice of Draftspe 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examine Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in 	erson's Patent Drawing Review(F _· er's Amendment / Comment or in t . 1.84(c)) should be written on the de	he Office action of	pack) of
 DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT 			e the
 Attachm nt(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB, Paper No./Mail Date Dec. 31, 2003 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. ☐ Interview Summ Paper No./Mail /08), 7. ☐ Examiner's Ame 8. ☐ Examiner's State 9. ☐ Other <u>Search Hi</u>	Date indment/Comment ement of Reasons for Allow	
		PHUNG	

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Detailed Office Action

1. In response to the U.S. Application Ser. No. 10/748,205 filed on December 31, 2003, claims **3-4** are pending in the application.

Examiner's amendment

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The specification has been amended as follows:

Page 1, after "November 27, 2002,", insert -- , **now U.S. Pat. No. 6,693,845**, issued on Feb. 17, 2004 --.

The examiner's amendment has been made in order to place the application in a condition for allowance.

Priority

3. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Information Disclosure Statement

4. This office acknowledges receipt of the following items from the Applicant: Information Disclosure Statement (IDS) filed December 31, 2003.
Information disclosed and listed on PTO 1449 was considered. Art Unit: 2824

The references: Arimoto et al. (U.S. Pat. No. 6,400,625), Lee (U.S. Pat. No. 6,078,533) and Manning (U.S. Pat. No. 5,946,244) have been entered on PTO 1449 and considered by the examiner.

Examiner's Statement of Reasons for Allowance

5. Claims **3-4** are allowed.

Arimoto et al. (U.S. Pat. No. 6,456,560), Maeda (U.S. Pat. No. 6,373,784) and Lee (U.S. Pat. No. 5,930,182) disclose a semiconductor device having a PPL circuit similar to that of the present application, but fail to teach "a logic circuit, which operates in synchronization with the internal clocks; a test clock terminal to which test clocks are supplied from an external circuit, the test clock having a frequency with a predetermined phase difference from the external clocks; a flip-flop circuit, which is supplied with the test clocks and the first internal clocks to generate second internal clocks; and an internal counter, which counts the second internal clocks when the PLL circuit is tested, wherein the internal counter is provided with an output terminal from which an output signal thereof is supplied to an external circuit" as claimed in the independent claim 3.

Any comments considered necessary by applicants must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comment on Statement of Reasons for Allowance".

Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **ANH PHUNG** whose telephone number is **(571) 272-**

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1883. The examiner can normally be reached on Monday-Friday from 8:00 AM to 4:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, RICHARD ELMS, can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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AKP

PRIMARY EXAMINER

Out Phung